FOR IMMEDIATE RELEASE

UCSB’s Professor Daniel Blumenthal to Address the Road to Terabit Ethernet at the Ethernet Technology Summit

Santa Clara, CA, February 18, 2011 – Daniel Blumenthal, Professor in the Electrical and Computer Engineering Department at the University of California, Santa Barbara and Director of the Terabit Optical Ethernet Center, will present at next week’s Ethernet Technology Summit at the Santa Clara Marriott in Santa Clara, California. Professor Blumenthal will discuss “The Road to Terabit Ethernet” in a workshop along with other Ethernet experts addressing “Beyond 100 Gigabit Ethernet: Terabit Ethernet” on Tuesday, February 22 from 8:30 am to 12 noon.

“We’re going to need much faster networking to handle the explosion in Internet traffic and support new large-scale applications like cloud computing,” said Professor Blumenthal.

As director of the Terabit Optical Ethernet Center (TOEC) at UCSB, Professor Blumenthal leads researchers in the development of the technology necessary for a new generation of Ethernet that is far faster, and much more energy efficient, than today’s most advanced networks. They are aiming for 1 Terabit Ethernet over optical fiber – 1 trillion bits per second – by 2015, with the ultimate goal of enabling 100 Terabit Ethernet by 2020. Partnering with TOEC as founding industry affiliates are Google, Verizon, Intel, Agilent Technologies and Rockwell Collins.

Workshop on Beyond 100 GbE - Terabit Ethernet
Organizer: Daniel Blumenthal, UCSB/TOEC
Chairperson: Brice Achkir, Cisco Systems
Paper Presenters:
The Road to Terabit Ethernet: Daniel Blumenthal, Professor - Electrical and Computer Engineering Dept., UCSB/TOEC
Standards Efforts Beyond 100GbE: John D’Ambrosia, Senior Scientist - Components Technology, Force10 Networks
Ethernet End Points Move to Quick Path Interconnect: Shre Shah, Architect, Xilinx
The Path to 1 TbE: Integration Using Silicon Photonics: Arlon Martin, VP Marketing, Kotura
Scalable Chip Platforms for TbE and Beyond: Eric Hall, VP Business Development, Aurrion
Course Description:
Even though the 40/100GbE specification has just been approved, it is now time to start thinking about a new generation. As 10GbE links become common in the data center, aggregation links will have to provide much higher throughput. A new generation will also be necessary for telecom backbones, cloud computing, wireless backhaul, supercomputing, video networks, and many other applications. We will need new standards, new signaling methods, new test equipment, new cabling and connectors, new power reduction approaches, and many other components and system-level elements. Starting now will mean that initial terabit Ethernet implementations could be available in the 2015-2016 period.

About Professor Daniel Blumenthal
Daniel Blumenthal is a Professor in the Electrical and Computer Engineering Department at the University of California, Santa Barbara. He is also Director of the Terabit Optical Ethernet Center (TOEC), which is developing the technology necessary for a new generation of Ethernet. Daniel’s research interests are in optical communications, photonic packet switching, all-optical networks, and ultra-fast communications. He was previously a professor at the Georgia Institute of Technology and an engineer for StorageTek. He has authored or co-authored over 350 papers, holds 7 patents, and is co-author of a book on laser diodes. He received his BSEE from the University of Rochester, his MSEE from Columbia University, and his Ph.D. from the University of Colorado.

About Ethernet Technology Summit
Ethernet Technology Summit focuses on the latest advances in Ethernet technology, particularly the new specifications for 40/100 GbE, Fibre Channel over Ethernet (FCoE), and energy-efficient Ethernet. It features the latest information on these advances from the experts who are developing the specifications and designing the initial products for them. It is a must-attend conference for those who are deciding whether to use the new specifications or design products for them or are in the initial stages of product design. The Summit also covers 10 GbE, Data Center Ethernet, carrier Ethernet, power-over-Ethernet, Ethernet security, Ethernet management, backplane Ethernet, and Ethernet chipsets. The Silicon Valley’s premier event focused on Ethernet also includes an expert table session, a market research session, and a customer review session. For more information, visit http://www.ethernetechnologysummit.com.

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